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	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKST NO.	CONFIRMATION NO.
•	09/704 176	10/31/2000	Blaine D Gaither	10007099-1	2685

TITLE OF INVENTION: CACHE COHERENCE PROTOCOL FOR A MULTIPLE BUS MULTIPROCESSOR SYSTEM

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he Director of the USPTO i OTE: The Issue Fee and Pu iterest as shown by the reco	s requested to apply the Issuablication Fee (if required) with the United States Parents of the United	re Fee and Publicat vill not be necepted not and Trademark	ion Fee (if an from anyon Office.	iy) or to re-apply any previously cother than the applicant, a regi	y paid issue fee to the applic stered automey or agent; or	ration identified above, the assignee or other party in
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PTOL-85 (Rev. 11/04) Approved for use through 04/30/2007.

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